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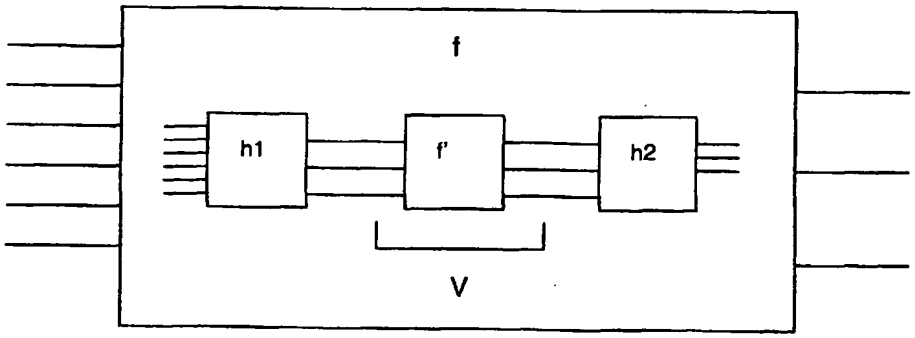
(54) Title: METHOD TO SECURE AN ELECTRONIC ASSEMBLY AGAINST ATTACKS BY ERROR INTRODUCTION



(57) Abstract: The invention concerns a method to secure an electronic assembly implementing any algorithm against attacks by error introduction. The method according to the invention consists in performing an additional calculation using a verification function on at least one intermediate result in order to obtain a calculation signature and in performing a least once more all or part of the calculation in order to recalculate said signature and compare them in order to detect a possible error.

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